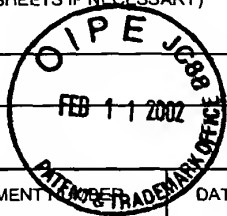


FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MICRON.248A	APPLICATION NO. 10/050,339
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT Tuttle et al.	
		FILING DATE January 15, 2002	GROUP Unknown



U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
MH	4,953,002	8/28/90	Nelson et al.			
MH	5,387,551	2/7/95	Mizoguchi et al.			
MH	5,561,265	10/1/96	Livshits et al.			
MH	5,640,047	6/17/97	Nakashima			
MH	5,902,690	5/11/99	Tracy et al.			
MH	5,939,772	8/17/99	Hurst et al.			
MH	6,027,948	2/22/00	Jensen et al.			

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
MH	"Easy BGA Packaging for Intel® Flash Memory Devices: Product Overview," Intel.com, (2001), pp. 1-3.
MH	"Overview Of Intel Packaging Technology," 1999 Packaging Databook, Ch. 1., pp.1-2.
MH	"Preliminary Mechanical and Shipping Media Information for Easy BGA Packages," Easy BGA Mechanical Specification, (May 2000), pp. 1-6.
MH	"The Chip Scale Package (CSP)," 2000 Packaging Databook, Ch. 15, pp. 1-12.
MH	Mahajan et al., "The Evolution of Microprocessor Packaging," Intel Technology Journal Q3, (2000), pp. 1-10.

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EXAMINER <i>Mahajan</i>	DATE CONSIDERED <i>05/03</i>
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